



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE PATENT APPLICATION

In re Application of:

Venkateswaran

Atty. Docket: TI-34105.1

Serial No.:

10/789,646

Art Unit: 2813

Filed:

03/01/2004

Examiner: J. Dolan

For:

SEMICONDUCTOR DEVICE PACKAGE AND METHOD OF MAKING THE

**SAME** 

Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

## **CERTIFICATE OF MAILING 37 CFR §1.8(a)**

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313-1450 on the date indicated below.

## LETTER TO THE OFFICIAL DRAFTSPERSON

Sir:

Please find enclosed eight (8) sheets of replacement formal drawings in the patent application referenced above.

Respectfully submitted,

Texas Instruments Incorporated P. O. Box 655474, M/S 3999 Dallas, Texas 75265

(972) 917-4371

Wade James Brady III Attorney for Applicant

Reg. No. 32,080